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Intellectual Property Law



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August 8, 2006

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Re: Title: INTEGRATED SEMICONDUCTOR FIN DEVICE AND A METHOD  
FOR MANUFACTURING SUCH DEVICE  
Letters Patent No. 6,974,729  
Issued: December 13, 2005  
Our Reference: IMEC280.001AUS

Dear Sir:

Enclosed for filing is a Certificate of Correction in connection with the above-identified patent.

As the errors cited in the Certificate of Correction were incurred through the fault of the Patent Office, no fee is believed to be required. However, please charge our Deposit Account No. 11-1410 for any fees that may be incurred with this request.

Respectfully submitted,

Knobbe, Martens, Olson & Bear, LLP

John M. Carson  
Registration No. 34,303  
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Enclosures

2813217:MRF  
080706

**Certificate**  
AUG 15 2006  
**of Correction**

AUG 15 2006

Orange County  
949-760-0404

San Francisco  
415-954-4114

Los Angeles  
310-551-3450

Riverside  
951-781-9231

San Luis Obispo  
805-547-5580

# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

**PATENT NO.** : 6,974,729  
**APPLICATION NO.** : 10/621,044  
**ISSUE DATE** : December 13, 2005  
**INVENTOR(S)** : Nadine Collaert and Kristin De Meyer

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On Page 1, Column 2, Line 4, Delete "FinFet" and insert -FinFET--, therefore.

On Page 2, Column 2, Line 13, Delete "V," and insert -V<sub>t</sub>-- therefore.

*unbold*

2813135  
080706

### MAILING ADDRESS OF SENDER:

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DOCKET NO. IMEC280.001AUS

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